

2671#

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Masanari ASANO



Art Unit: 2671

Serial No.: 09/813,035

Examiner: S. Wallace

Filed: March 21, 2001

Docket No.: 024354-00001

For: IMAGE PROCESSOR WITH THE CLOSED CAPTION FUNCTION AND
IMAGE PROCESSING METHOD

RECEIVED

MAR 20 2003

AMENDMENT COVER SHEET

Technology Center 2600

Commissioner of Patents
Washington, D.C. 20231

March 14, 2003

Sir:

Transmitted herewith is an Amendment in the above-identified application.

☐ Small Entity status is claimed

FEE TRANSMITTAL

	Claims Remaining After Amendment	Highest Number Previously Filed	Present Extra	Rate	Basic Fee
Total Claims	17	20	0	x \$18.00	
Independent Claims	4	3	1	x \$84.00	84.00
Multiple Dependent Claims				\$280.00	
Reduction by 1/2 for small entity					
TOTAL					84.00

Masanari ASANO

U.S. Appln. No. 09/813,035
Docket No.: 024354-00001

☒ Check in the amount of **\$ 84.00**. (\$ 84.00 for the extra claim fee and) is attached. Please charge any fee deficiency or credit any overpayment to Deposit Account No. 01-2300. to cover the filing fee is attached.

Respectfully submitted,

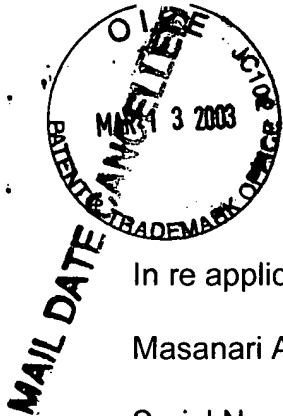
Arent Fox Kintner Plotkin & Kahn, PLLC



Raymond J. Ho
Registration No. 41,838

Customer No. **004372**
1050 Connecticut Avenue, N.W.
Suite 400
Washington, D.C. 20036-5339
Tel: (202) 857-6000
Fax: (202) 638-4810

RJH/klf



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#3
Amend A
3-2003
MDH

In re application of:

Masanari ASANO

Serial No.: 09/813,035

Filed: March 21, 2001

For: IMAGE PROCESSOR WITH THE CLOSED CAPTION FUNCTION AND
IMAGE PROCESSING METHOD



Confirmation: 2760

Art Unit: 2671

Examiner: S. Wallace

Docket No.: 024354-00001

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AMENDMENT

MAR 20 2003

Commissioner for Patents
Washington, D.C. 20231

Technology Center 2600
March 14, 2003

Dear Sir:

In response to the Office Action dated December 19, 2002 (hereinafter
"outstanding Action"), please reconsider the above-identified application amended as
follows:

IN THE CLAIMS:

Please added new claims 16 and 17, and amend claim 1 as follows (a
marked-up version of the amended claim 1 is enclosed as required by 37 C.F.R. §
1.121):

1. (Amended) An image processor comprising:

a storage circuit storing therein image data;

a data input/output circuit controlling input/output of the image data;

an access control circuit controlling access of writing in and reading out

the image data to and from said storage circuit;

Sub
B1
A1